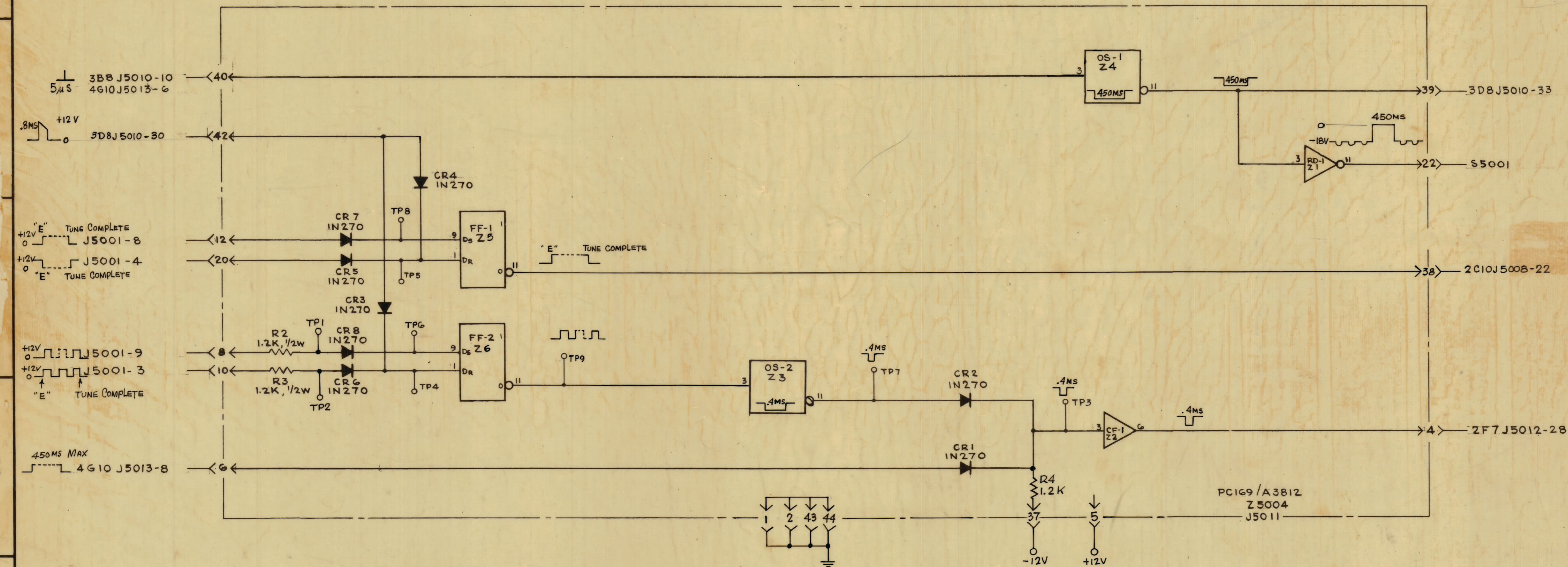


CARDS USED

PART NO.	DESCRIPTION	CON. NO.	LOCATION	B+ INPUT	B- INPUT	GROUND
PC127/A3694	INITIAL RESET CIRCUIT	J5007	SHEET 2	PIN 5	PIN 37	1, 2, 43, 44
PC165/A3806	SERIAL TO PARALLEL CLOCK CIRCUIT	J5008		2	5	37
PC125/A3692	SERIAL TO PARALLEL SHIFT REGISTER	J5009		2	5	37
PC170/A3813-1	STUNTING CARD "J"	J5010		3	5	37
OR PC191/A3813-2	STUNTING CARD "S"	J5010		3	5	37
PC169/A3812	ADVANCE, TUNE COMPLETE, BOY RELAY CKT.	J5011		1	5	37
PC175/A3823	ADVANCE PRIME TIMING CIRCUIT	J5012		2	5	37
PC171/A3820	MEMORY CORE INPUT SHIFT REGISTER	J5013		4	5	37
PC173/A3821	PRIME CIRCUIT CARD	J5014		3	5	37
PC176/A3824	ADVANCE 'O' & 'E' CIRCUIT	J5015		3	5	37
PC182/A3639	MEMORY CORE REGISTER	J5016		4	6	2
		J5017		4	6	2
		J5018		4	6	2
		J5019		4	6	2
PC182/A3639	MEMORY CORE REGISTER	J5020		4	6	2
PC158/A3800	DRIVE CIRCUIT FOR DECODER RELAYS	J5021	SHEET 4	5	37	1, 2, 43, 44

REVISIONS			
Ø	ORIGINAL RELEASE FOR PRODUCTION	5/8/65	NLA
A	SEE EXACTS 202* AND 4.05*	5/22/65	1/22/65



DWG DESTINATION  
 4 G10 J5013-8  
 SHEET NO. ZONE CONNECTOR NO. PIN NO.

REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYMBOL
EATON LIST OF MATERIAL				
MATERIAL THE TECHNICAL MATERIEL CORP. MAMARONECK, NEW YORK				
FINISH TITLE DIAGRAM, LOGIC, DETAILED RTMU-1				
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES				
DRAWN BY	RAMIREZ	DATE	1/6/65	FINAL APPROVAL
CHECKED BY	DOB	DATE	3/3/65	DATE
ELECT. DES.		DATE	5/2/65	DATE
MECH. DES.		DATE		DATE
DECIMALS	± .05	FRACTIONS	± 1/64	CK806
XX ± .005		TOLERANCES	ANGLES ± 0° 30'	A
SHEET 1 OF 4				REV. LTR.