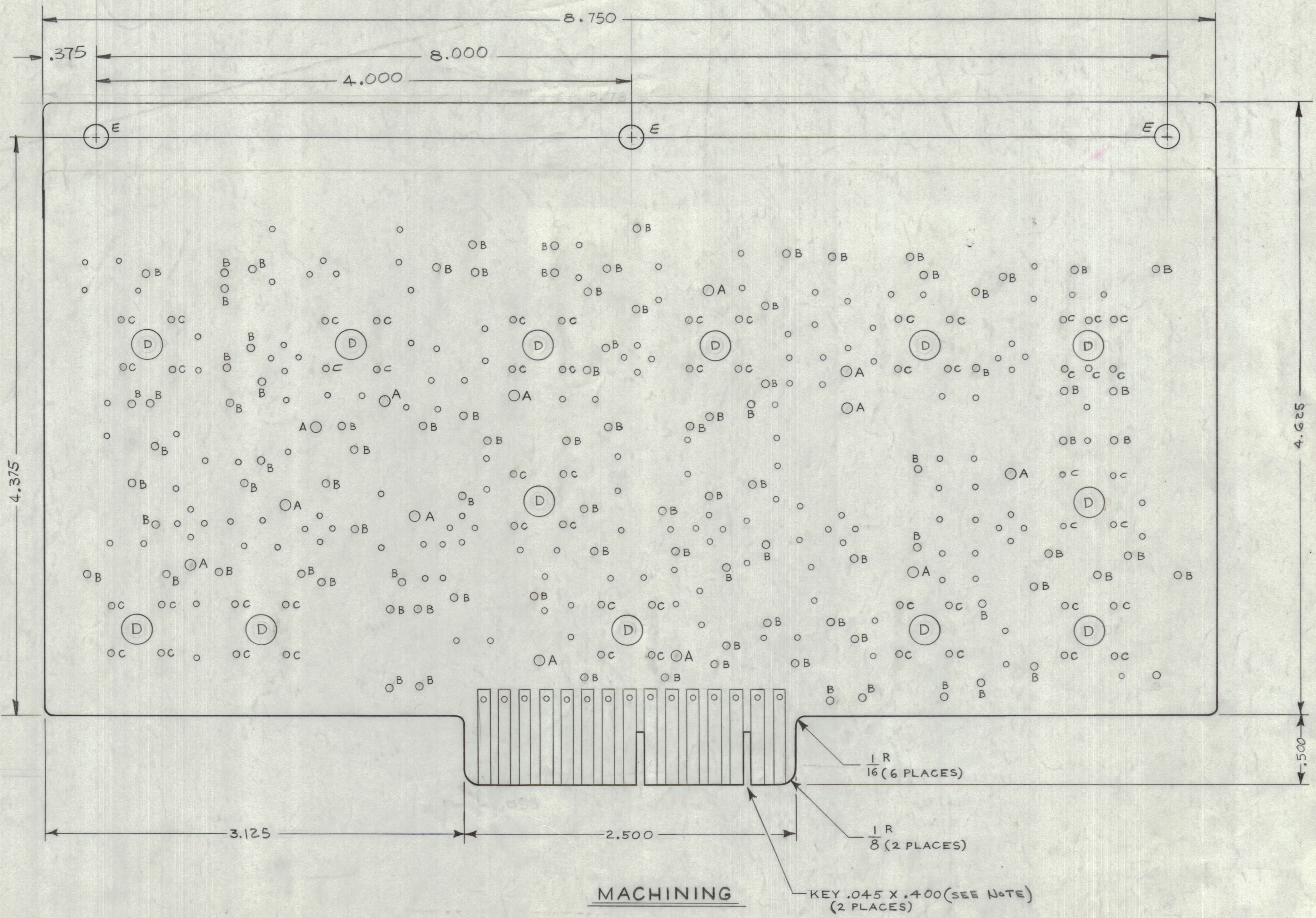


REVISIONS							
ZONE	SYM	DESCRIPTION	DATE	E.M.N. NO.	DRAFT	CHKD	APPD
	X1	EXPERIMENTAL RELEASE	10-26-64		NP		
	X2	DOTS FOR 4-LEAD TRANSISTORS ADD; L907 WAS FL901; L908 WAS FL902; L909 WAS FL903; L910 WAS FL904; L911 WAS FL905; L912 WAS FL906; L913 WAS FL907; L914 WAS FL908; L915 WAS FL909.	12/31/64	X2	JP		
A2	X3	ITEM (1) ADDED E HOLES ADDED; DIM. 4.625 WAS 4.156	1-12-65	X3	JP	AK	JPB
	X4	DIM. REVISED, NOTES REVISED, ITEM (1) WAS -3					
		ORIGINAL RELEASE FOR PRODUCTION	5/28/65	X5	JP		



HOLE LEGEND		
HOLE	DESCRIPTION	REQD
A	#46 DR (.081)	13
B	#55 DR (.052)	* 96
C	#53 DR (.059)	* 54
D	17/64 DR (.265)	13
UN-MARKED	#60 DR (.040)	* 178
E	11/64 DIA	3

\* INDICATES PLATED-THRU HOLE

FABRICATION NOTES

- MATERIAL THICKNESS .062 ± .0075 AS PER 3.2 13.2.2
- .040 DIA. PLATED THRU HOLES. TO BE LOCATED IN THE CENTER OF EACH FINGER CONTACT .050 AWAY FROM THE MAIN BODY OF BOARD.
- THE PRINTED CIRCUIT BOARDS SHALL BE KEYED WITH SLOTS .050 - .070 WIDE & .400 HIGH LOCATED BETWEEN FINGER CONTACTS 8 & 9, 13, 14 AS VIEWED FROM COMPONENT SIDE OF BOARD.
- EDGE BOARD CONNECTOR (FINGERS) SHALL BE GOLD PLATED 10.1 CONDUCTIVE PATTERN SHALL BE SOLDER COATED (PP13.3 & PP13.3.1)
- HOLES TO BE PLATED THRU AS PER (PP11.4)
- KEY SLOTS TO BE DEBURRED.
- FOR SPARE PARTS USE ON MODEL LFSB-1 SERIAL NO. 16336 THRU 16361, BOARD THICKNESS MUST BE .093 (3/32) ITEM 1 MUST ALSO CHANGE TO TE127-3.

PC157

13	1	TE127-2	TERMINAL STUD	A
REQ'D	ITEM	PART NUMBER	DESCRIPTION	SYMBOL
M. GELLMAN LIST OF MATERIAL				
MATERIAL		SEE NOTE		
FINISH		//		
THE TECHNICAL MATERIEL CORP.		MAMARONECK, NEW YORK		
TITLE		PRINTED CIRCUIT BOARD		
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND INCLUDE CHEMICALLY APPLIED OR PLATED FINISHES	DRAWN	DATE	FINAL APPROVAL	DATE
DECIMALS ± .05 XX ± .01 XXX ± .005	2. Petera	10-26-64	JP	6/1/65
FRACTIONS ± 1/64 ANGLES ± 0° 30'	CHECKED	DATE		
	JP	1-29-65		
	ELECT. DES.	DATE		
	MP	5-28-65		
	MECH. DES.	DATE		
	MP	5-28-65		
NOTES		PC157		
		SHEET 2 OF 2		